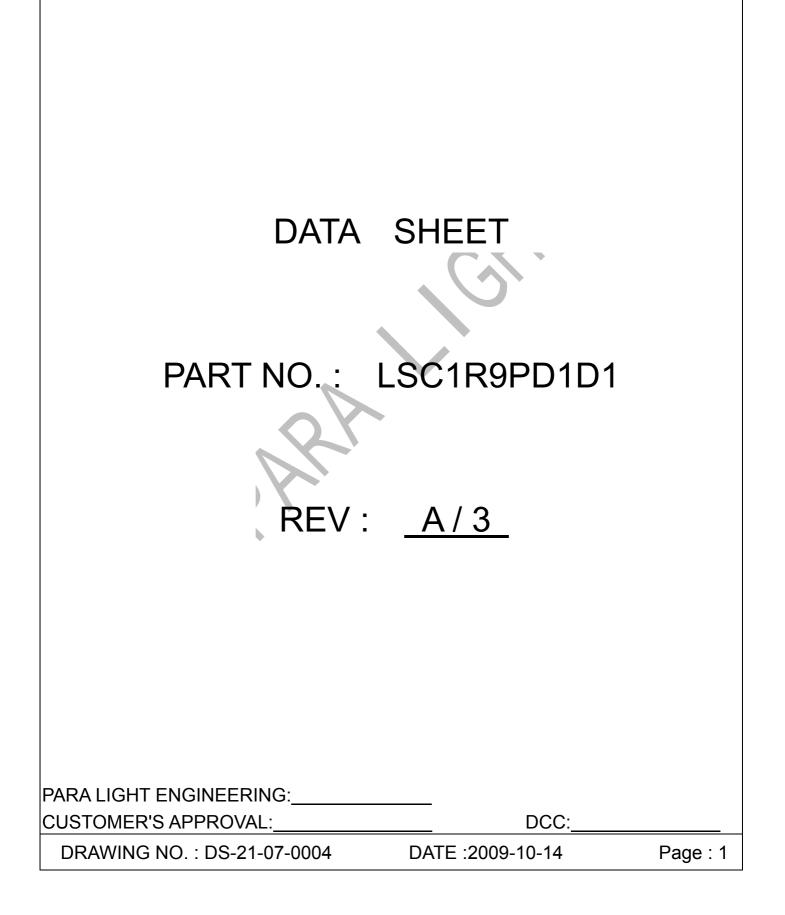


PARA LIGHT ELECTRONICS CO., LTD.

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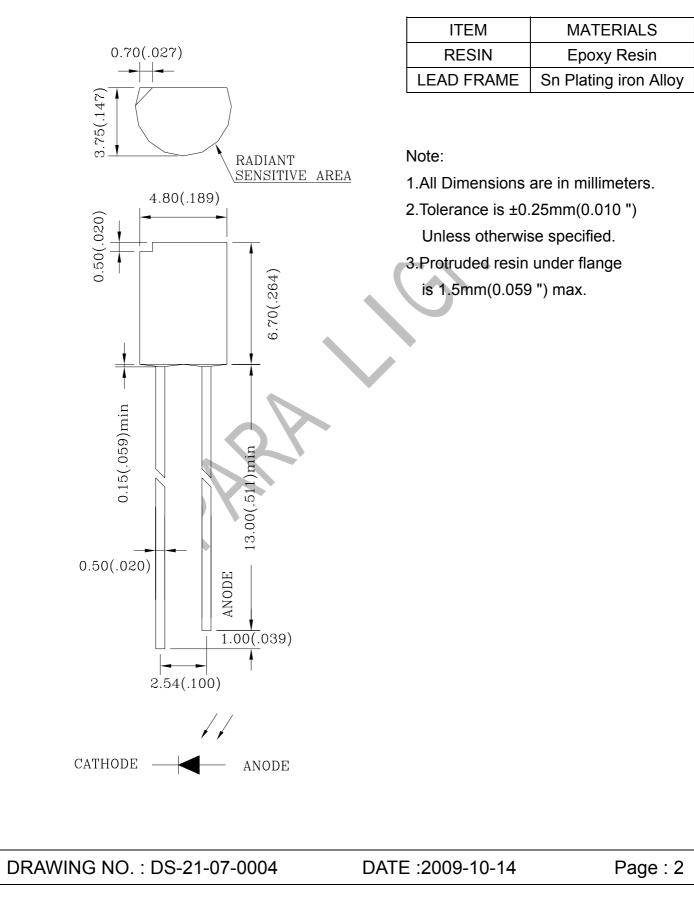




LSC1R9PD1D1

REV:A/3

PACKAGE DIMENSIONS





LSC1R9PD1D1

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FEATURES

- * WIDE RANGE COLLECTOR CURRENTS
- * LENSED FOR HIGH SENSITIVITY
- * HIGH OUTPUT POWER
- * HIGH SPEED RESPONSE
- * Pb FREE PRODUCTS

CHIP MATERIALS

* SILICON

ABSOLUTE MAXIMUM RATING : (Ta = 25°C)

SYMBOL	PARAMETER	MAX	UNIT
PD	Power Dissipation	150	mW
VR	Reverse Voltage	30	V
Topr	Operating Temperature Range	-25°C to 85°C	
Tstg	Storage Temperature Range	-25°C to 85°C	

ELECTRO-OPTICAL CHARACTERISTICS : (Ta = 25°C)

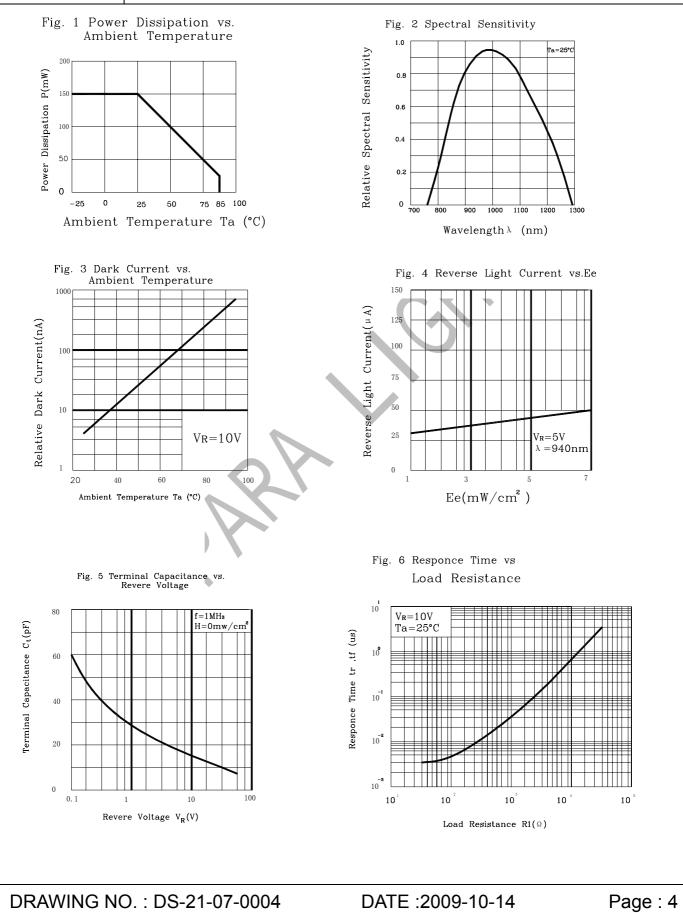
In						
SYMBOL	PARAMETER	TEST	MIN	TYP	MAX	UNIT
		CONDITION				
ID	Reverse Dark Current	VR= 10V		5	30	nA
		$Ee = 0 mw/cm^2$				
V(BR)R	Reverse Voltage	IR=100μA	33	170		V
		Ee= 0 mw/cm ²				v
Voc	Open Circuit Voltage	λP=940nm		0.35		v
		Ee=5 mw/cm ²		0.00		v
IL	Light Current	VR=5V Ee=5 mw/cm ²	30	40		uA
		VR=10V				
ton/toff	Turn-ON Turn-OFF Time	-		45/45		uS
		RL=1000Ω				
Ст	Total Capacitance	f=1MHZ		18		
		VR=5V				PF
		Ee=0 mw/cm ²				
λΡ	Peak Sensitmty Wavelength			940		nm

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Label Explanation

PCS g 7 000 E F	09
7 000	09
	<u>09</u>
- F	=



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•SOLDERING

METHOD	SOLDERING CONDITION	IS REMARK		
DIP SOLDERING	Bath temperature: 260℃ Immersion time: within 5 sec,	I leina solderina flux " RE		
SOLDERING IRON	Soldering iron: 30W or sma Temperature at tip of iron: 300℃ Soldering time: within 3 se	or lower (To prevent heat from beir c. transferred directly to th the lead with a pair of tw while soldering)	inst the ng e lead, hold veezers	
		that the package is fixed with a pan	el (See Fig.1),	
be careful not	t to stress the leads with iron tip.			
2) When solderi	Panel (Fig. 1) ng wire to the lead, work with a F	ig (See Fig.2) to avoid stressing the	e package.	
۵				
Leave a cle		Lead wries (Fig. 2)		
Regarding tinning the leads, compound made of tin ,copper and sliver is proposed with the temperature of 260° . The proportion of the alloyed solution is 95.5% tin, 3.5% copper, 0.5% silver. The time of tinning is 3 seconds.				
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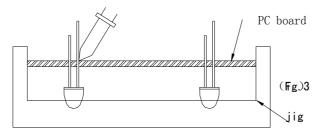
•

BLACK PLASTIC PHOTODIODE

LSC1R9PD1D1

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3) Similarly, when a jig is used to solder the LED to PC board, take care as much as possible to avoid stressing the leads (See Fig.3).



- Repositioning after soldering should be avoided as much as possible. If inevitable: select a best-suited method that assures the least stress to the LED.
- 5) Lead cutting after soldering should be performed only after the LED temperature has returned to normal temperature.

• STORAGE

- 1) The LEDs should be stored at 30 $^\circ$ C or less and 70% RH or less after being shipped from PARA and the storage life limit is 1 year .
- 2) PARA LED lead frames are comprised of a tin plated iron alloy. The surface may be affected by environments which contain corrosive gases and so on. Please avoid conditions which may cause the LEDs to corrode, tarnish or discolor. This corrosion or discoloration may cause difficulty during soldering operations. It is recommended that the LEDs be used as soon as possible.
- 3) Please avoid rapid changes in ambient temperature, especially, in high humidity environments where condensation can occur.

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• STATIC ELECTRICITY

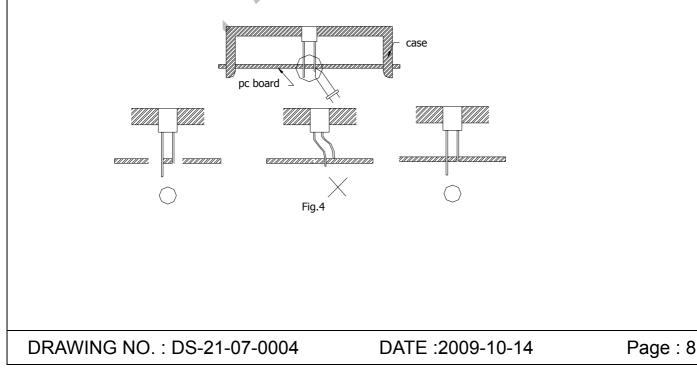
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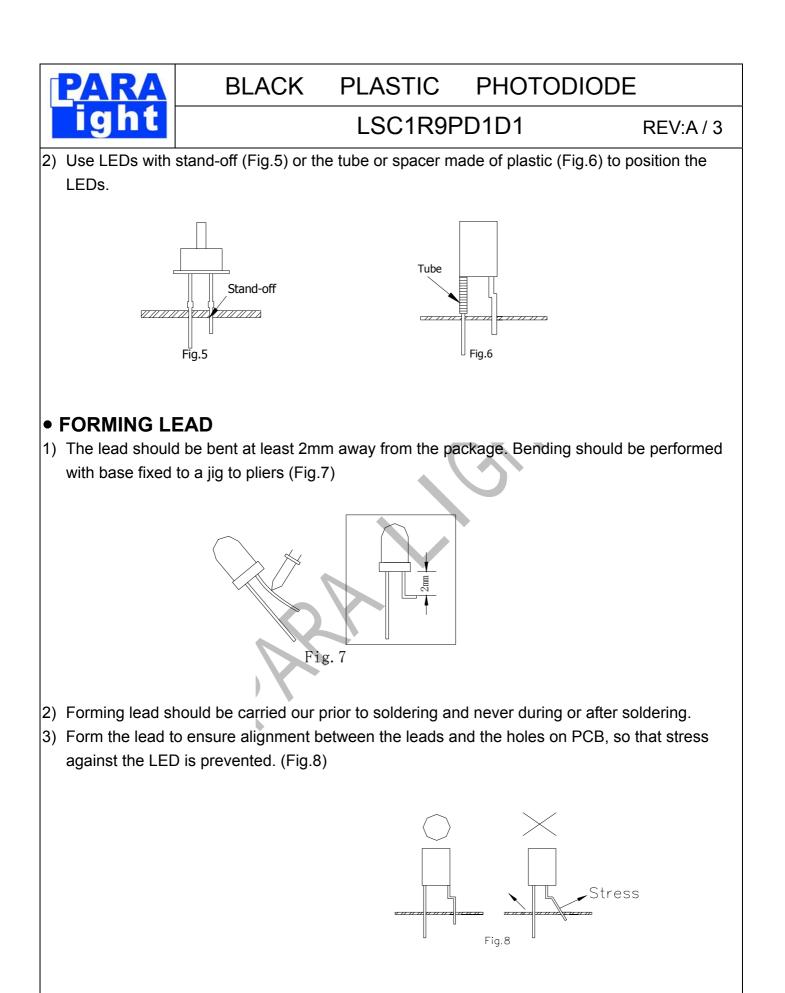
- Static electricity or surge voltage damages the LEDs.
 It is recommended that a wrist band and an anti-electrostatic glove be used when handling the LEDs.
- 2) All devices, equipment and machinery must be properly grounded. It is recommended that measures be taken against surge voltage to the LED mounting equipment.
- 3) When inspecting the final products in which LEDs were assembled, it is recommended to check whether the assembled LEDs are damaged by static electricity. To find static-damaged LEDs, perform a light-on test or a VF test at a lower current (below 1mA is recommended).
- 4) Damaged LEDs will show some unusual characteristics such as the leakage current remarkably increases, the forward voltage becomes lower, or the LEDs do not light at the low current.

Criteria : (VF>2.0V at IF=0.5mA)

• LED MOUNTING METHOD

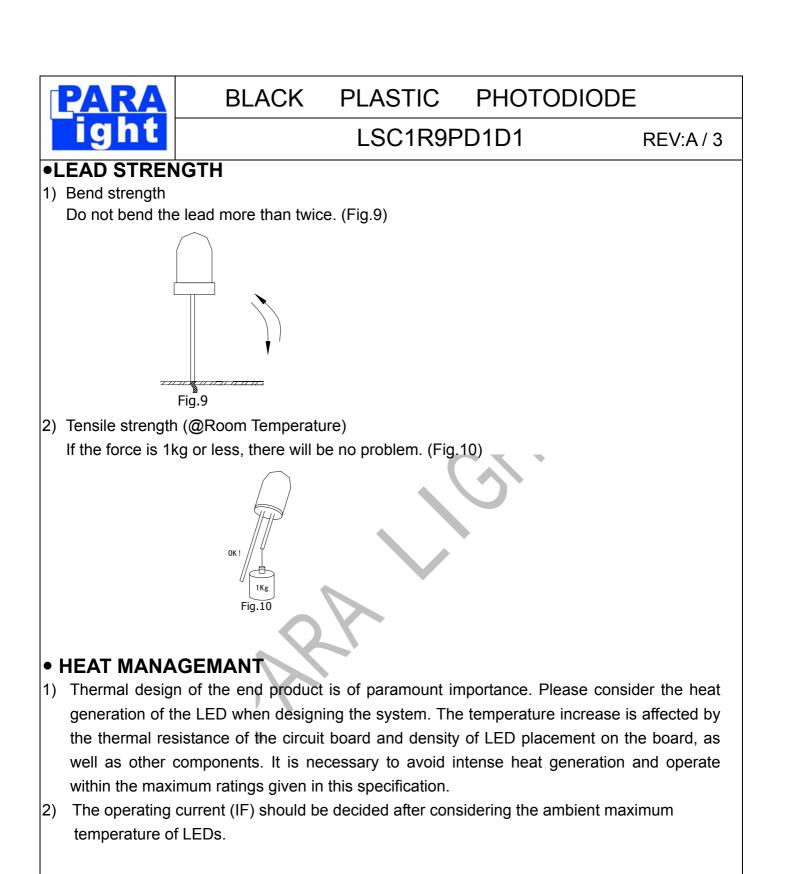
1) When mounting the LED to a housing, as shown on Fig.4, ensure that the mounting holes on the PC board match the pitch of the leads correctly. Tolerance of dimensions of the respective components including the LEDs should be taken into account especially when designing the housing, PC board, etc. to prevent pitch misalignment between the leads and holes on PCB, the diameter of the holes should be slightly larger than the size of the lead. Alternatively, the shape of the holes could be made oval. (See Fig.4)





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•CHEMICAL RESISTANCE

- 1) Avoid exposure to chemicals as it may attack the LED surface and cause discoloration.
- When washing is required, refer to the following table for the proper chemical to be used. (Immersion time: within 3 minutes at room temperature.)

SOLVENT	ADAPTABILITY
Freon TE	\odot
Chlorothene	\times
Isopropyl Alcohol	\odot
Thinner	\times
Acetone	\times
Trichloroethylene	×

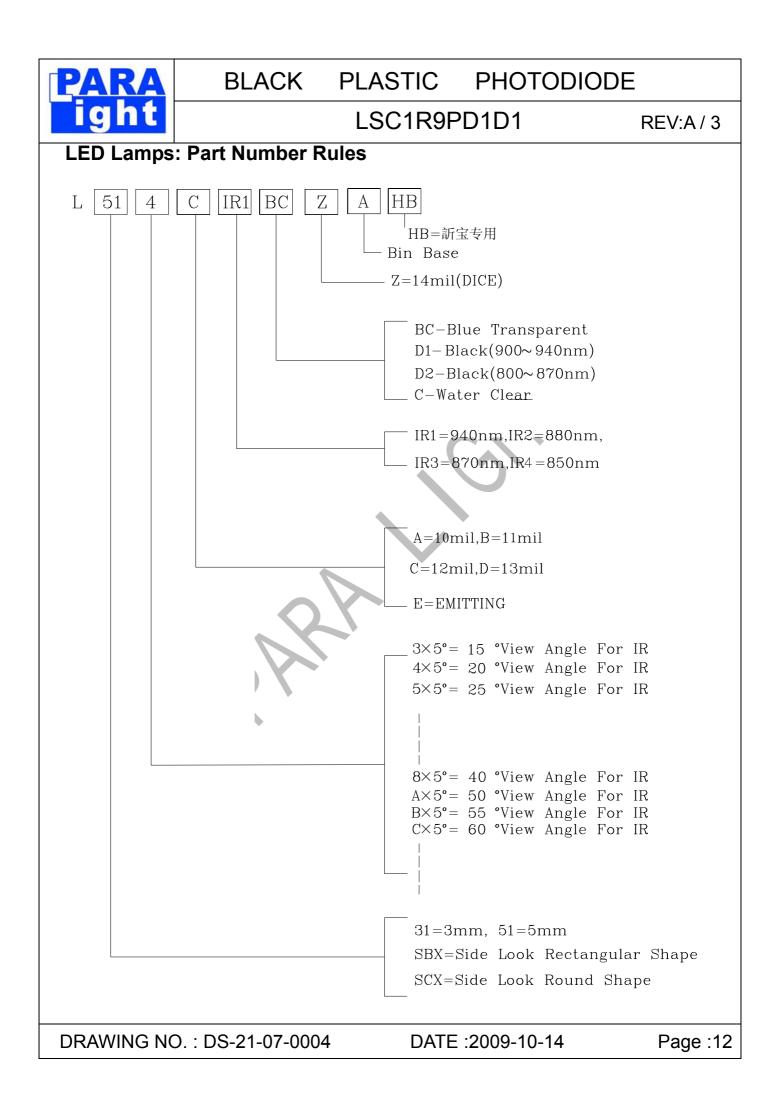
 \odot --Usable \times --Do not use.

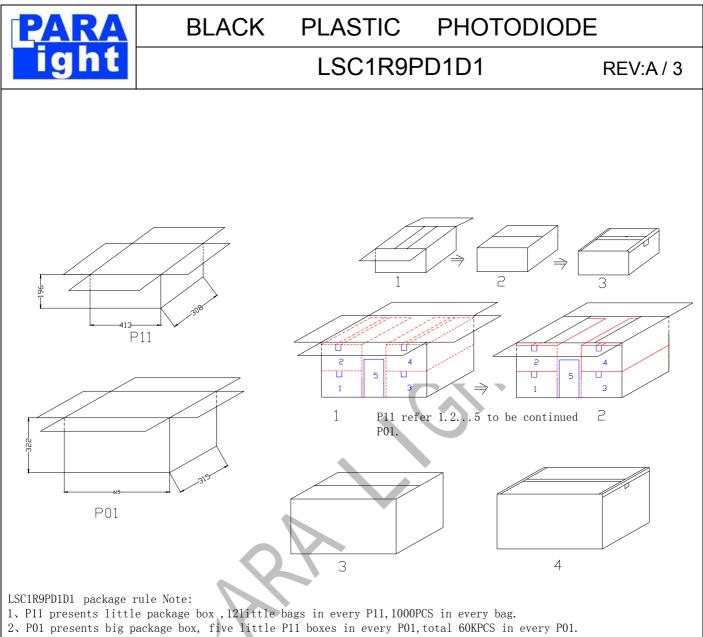
•OTHER CONSIDERTIONS

resin body differ depending on factors such as the oscillator output, size of the PC board and the way in which the LED is mounted. Therefore, ultrasonic cleaning should only be performed by confirming an ultrasonic cleaning trial run.

NOTE: Influences of ultrasonic cleaning of the LED

- 1) Care must be taken to ensure that the reverse voltage will not exceed the absolute maximum rating when using the LEDs with matrix drive.
- 2) The LEDs described in this data sheet are intended to be used for ordinary electronic equipment (such as office equipment, communications equipment, measurement instruments and household appliances). Consult PARA's sales staff in advance for information on the applications in which exceptional quality and reliability are required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as for airplanes, spacecraft, automobiles, traffic control equipment etc).
- The formal specifications must be exchanged and signed by both parties before large volume purchase begins.





- 3. Specific package course refers to the attached graph.

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